

IN THE CLAIMS:

1. (Currently amended) A high speed mass storage system which is readily expandable to increase its storage capacity while the system is in operation ~~comprising at least one module containing~~ comprising:

[[a)] at least one CPU;

first and second mass storage modules each including

[[b)] (a) a plurality of plug-in storage devices for storing information;

[[c)] (b) a storage device bypass circuit board associated with each storage device, each storage device being plugged into a connector on the storage device bypass circuit board;

[[d)] (c) a module bypass circuit board including an optical input/ output connector for outputting electrical signals from the module as light signals and for inputting light signals into the module as electrical signals, and wherein the optical input/output connectors of the first and second mass storage modules are connected by a fiber optic transmission medium such that signals are communicated between the first and second mass storage modules in the form of light; [[and,]] and

[[e)] a controller providing a communication path between the CPU with each of the storage devices through its associated storage device bypass circuit board and through the module bypass circuit board.

2. (Previously presented) The high speed mass storage system of claim 1 wherein each storage device bypass circuit board includes a circuit which completes the

connection of the CPU with the other storage device bypass circuits and their associated storage devices whether or not the storage device is present.

3. (Currently amended) The high speed mass storage system of claim 1 wherein ~~[[the]]~~ each said module bypass circuit board outputs electrical signals from the corresponding mass storage ~~at least one~~ module via the optical input/output connector when light signals are received by said optical input/output connector.

4. (Cancelled) The high speed mass storage system of claim 1 including first and second modules each including elements (a) through (e), wherein the optical input/output connectors of the modules are connected by a fiber optic transmission medium such that signals are communicated between the modules in the form of light.

5. (Currently amended) The high speed mass storage system of ~~claim 4~~ claim 1 wherein the module bypass circuit board of the first mass storage module outputs electrical signals from the first mass storage module to the second mass storage module via the optical input/output connector when light signals are received from the second mass storage module by said optical input/output connector.

6. (Currently amended) The high speed mass storage system of claim 1 wherein ~~the at least one~~ each said mass storage module includes a storage device bypass board connector for each of the storage device bypass circuit boards with an opening between each connector to permit the flow of air between the connectors and alongside the bypass circuit boards and storage devices for cooling purposes.

7. (Previously presented) The high speed mass storage system of claim 1 wherein the storage devices are disk drives and the storage device bypass circuit boards are disk drive bypass circuit boards each having a connector to receive a disk drive.

8. (Currently amended) The high speed mass storage system of claim 7 wherein ~~the at least one~~ each said mass storage module includes a disk drive bypass circuit board connector for each of the drive bypass circuit boards with an opening between each connector to permit the flow of air between the connectors, and wherein each drive bypass circuit board is a relatively flat circuit board with a connector on opposite edges, wherein one of the connectors is the connector which receives the disk drive and the other connector connects to said drive bypass circuit board connector, said connectors, bypass circuit boards and drives being arranged such that when they are connected there is a path for air flow from outside the module alongside each bypass circuit board and its associated disk drive for cooling purposes without any backplane obstruction.

9. (Currently amended) The high speed mass storage system of claim 8 wherein ~~[[the]]~~ each said mass storage module is housed in an enclosure and at least one fan is mounted to force air from outside said enclosure through the spaces between said bypass boards and drives.

10. (Previously presented) The high speed mass storage system of claim 1 wherein the controller operates with a ~~[[Fiber]]~~ Fibre Channel protocol.

11. (Previously presented) The high speed mass storage system of claim 1 wherein the controller is an arbitrated dual channel ~~[[Fiber]]~~ Fibre Channel controller.

12. (Previously presented) The high speed mass storage system of claim 10 wherein each storage device is a disk drive and wherein each storage device bypass circuit board comprises a disk drive bypass circuit board including a circuit which completes the connection of the CPU with the other drive bypass circuits and their associated disk drives whether or not the disk drive is present.

13. (Currently amended) The high speed mass storage system of claim 12 wherein the module bypass circuit board outputs electrical signals from ~~the at least one~~ each said mass storage module via the optical input/output connector when light signals are received by said optical input/output connector.

14. (Cancelled) The high speed mass storage system of claim 11 including first and second modules each including elements (a) through (e), wherein the optical input/output connectors of the modules are connected by a fiber optic transmission medium such that signals are communicated between the modules in the form of light.

15. (Cancelled) The high speed mass storage system of claim 14 wherein the module bypass circuit board of the first module outputs electrical signals from the first module to the second module via the optical input/output connector when light signals are received from the second module by said optical input/output connector.

16. (Previously presented) The high speed mass storage system of claim 11 wherein the storage devices are disk drives and the storage device bypass circuit boards are disk drive bypass circuit boards, and wherein the at least one module includes a disk drive bypass board connector for each of the disk drive bypass circuit boards with an

opening between each connector to permit the flow of air between the connectors and alongside the bypass circuit boards and disk drives for cooling purposes.

17. (Currently amended) The high speed mass storage system of claim 12 wherein ~~the at least one~~ each said mass storage module includes a disk drive bypass circuit board connector for each of the disk drive bypass circuit boards with an opening between each connector to permit the flow of air between the connectors, and wherein each disk drive bypass circuit board is a relatively flat circuit board with a connector on opposite edges, wherein one of the connectors is the connector which receives the disk drive and the other connector connects to said disk drive bypass circuit board connector, said connectors, bypass circuit boards and drives being arranged such that when they are connected there is a path for air flow from outside ~~[[the]]~~ each said mass storage module alongside each bypass circuit board and its associated disk drive for cooling purposes without any backplane obstruction.

18. (Currently amended) The high speed mass storage system of claim 17 wherein ~~[[the]]~~ each said mass storage module is housed in an enclosure and at least one fan is mounted to force air from outside said enclosure through the spaces between said bypass boards and drives.

19. (Currently amended) A high speed mass storage system adapted to be readily expandable to increase its capacity while the system is in operation ~~comprising at least one module containing~~ comprising:

~~[[a]]~~ at least one CPU;

first and second mass storage modules each including:

[[[b)]]] (a) a plurality of plug-in disk drives for storing information;

[[[c)]]] (b) a disk drive bypass circuit board associated with each disk drive and including a disk drive connector at one edge thereof and a bypass board connector at another edge thereof, each disk drive being plugged into said disk drive connector on the disk drive bypass circuit board, and wherein the optical input/output connectors of the first and second mass storage modules are connected by a fiber optic transmission medium such that signals are communicated between the modules in the form of light;

[[[d)]]] (c) a module bypass circuit board including an optical input/ output connector for outputting electrical signals from the module as light signals and for inputting light signals into the module as electrical signals; and,

[[[e)]]] a controller connecting the CPU with each of the disk drives through its associated drive bypass circuit board and through the module bypass circuit board such that a loop is formed between the output and input of the controller with each disk drive bypass circuit board and the module bypass circuit board in said loop and completing said loop whether or not a disk drive is plugged into the disk drive connector.

20. (Currently amended) The high speed mass storage system of claim 19, wherein including a second module connected to said at least one module via the optical input/output connector; said module bypass circuit board of the at least one said first mass storage module completing completes said loop through the second module.

21. (Currently amended) The high speed mass storage system of claim 19 wherein the module bypass circuit board outputs electrical signals from the at least one

each said mass storage module via the optical input/output connector when light signals are received by said optical input/output connector.

22. (Cancelled) The high speed mass storage system of claim 20 wherein each of said modules includes elements (a) through (e), wherein the optical input/output connectors of the modules are connected by a fiber optic transmission medium such that signals are communicated between the modules in the form of light.

23. (Currently amended) The high speed mass storage system of claim 22 wherein the module bypass circuit board of ~~the at least one~~ each said mass storage module outputs electrical signals from the ~~at least one~~ first mass storage module to the second mass storage module via the optical input/output connector when light signals are received from the second mass storage module by said optical input/output connector.

24. (Currently amended) The high speed mass storage system of claim 19 wherein ~~the at least one~~ each said mass storage module includes a drive bypass board connector for each of the drive bypass circuit boards with an opening between each connector to permit the flow of air between the connectors and alongside the bypass circuit boards and disk drives for cooling purposes.

25. (Currently amended) The high speed mass storage system of claim 19 wherein ~~the at least one~~ each said mass storage module includes a drive bypass circuit board connector for each of the drive bypass circuit boards, and wherein each drive bypass circuit board is a relatively flat circuit board with a connector on opposite edges, wherein one of the connectors is the connector which receives the disk drive and the other connector connects to said drive bypass circuit board connector, said connectors,

bypass circuit boards and drives being arranged such that when they are connected there is a path for air flow from outside ~~the~~ each said mass storage module alongside each bypass circuit board and its associated disk drive for cooling purposes without any backplane obstruction.

26. (Currently amended) The high speed mass storage system of claim 25 wherein ~~the at least one~~ each said mass storage module is housed in an enclosure and at least one fan is mounted to force air from outside said enclosure through the spaces between said bypass boards and drives.

27. (Previously presented) The high speed mass storage system of claim 26 wherein each drive bypass circuit board connector is mounted in the same plane in spaced relationship with each other.

28. (Previously presented) The high speed mass storage system of claim 19 wherein the controller operates with a ~~[[Fiber]]~~ Fibre Channel protocol.

29. (Previously presented) The high speed mass storage system of claim 19 wherein the controller is an arbitrated dual channel ~~[[Fiber]]~~ Fibre Channel system.

30. (Previously presented) The high speed mass storage system of claim 29 wherein each drive bypass circuit board includes a circuit which completes the connection of the CPU with the other drive bypass circuits and their associated disk drives whether or not the disk drive is present.

31. (Currently amended) The high speed mass storage system of claim 29 wherein the module bypass circuit board outputs electrical signals from ~~the at least one~~



each said mass storage module via the optical input/output connector when light signals are received by said optical input/output connector.

32. (Cancelled) The high speed mass storage system of claim 29 including first and second modules each including elements (a) through (e), wherein the optical input/output connectors of the modules are connected by a fiber optic transmission medium such that signals are communicated between the modules in the form of light.

33. (Cancelled) The high speed mass storage system of claim 32 wherein the module bypass circuit board of the first module outputs electrical signals from the first module to the second module via the optical input/output connector when light signals are received from the second module by said optical input/output connector.

34. (Currently amended) The high speed mass storage system of claim 29 wherein ~~the at least one~~ each said mass storage module includes a drive bypass board connector for each of the drive bypass circuit boards with an opening between each connector to permit the flow of air between the connectors and alongside the bypass circuit boards and disk drives for cooling purposes.

35. (Currently amended) The high speed mass storage system of claim 29 wherein ~~the at least one~~ each said mass storage module includes a drive bypass circuit board connector for each of the drive bypass circuit boards with an opening between each connector to permit the flow of air between the connectors, and wherein each drive bypass circuit board is a relatively flat circuit board with a connector on opposite edges, wherein one of the connectors is the connector which receives the disk drive and the other connector connects to said drive bypass circuit board connector, said connectors,

bypass circuit boards and drives being arranged such that when they are connected there is a path for air flow from outside ~~the~~ each said mass storage module alongside each bypass circuit board and its associated disk drive for cooling purposes without any backplane obstruction.

36. (Currently amended) The high speed mass storage system of claim 35 wherein ~~the~~ each said mass storage module is housed in an enclosure and at least one fan is mounted to force, air from outside said enclosure through the spaces between said bypass boards and drives.